

- ChipNet**
- 3 - 16 Pin Single In-Line Network
 - SMD - Chip assembled
 - Special circuit layouts upon request
 - resetable fuses possible

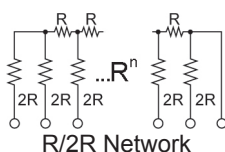
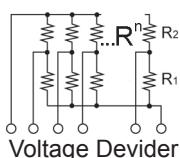
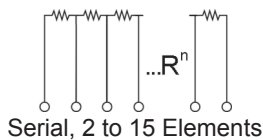
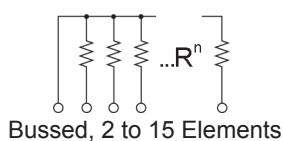
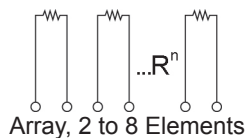
- ChipPac**
- 4 - 16 Pin SIP isolated resistors
 - Combinations with other components
 - Up to 8 individual resistive elements
 - Power dissipation up to 0.5 Watts

MECHANICAL SPECIFICATIONS	
Material	NiCr/ RuO ₂
Substrate Material	Alumina
Body	Epoxy - coated
Terminals	Copper
Plating	Tin
Storage Temperature Range	-20°C to 125°C

All dimensions in mm

Number of Pins	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Length L in mm (±0,5)	7,6	10,2	12,7	15,0	17,7	20,3	22,8	25,4	27,9	30,4	33,0	35,5	38,1	40,6

Standard Circuits



ELECTRICAL SPECIFICATIONS			
Standard Resistance Range	1Ω - 1GOhm		
Temperature Coefficient	Tracking	from 5ppm (depending on values)	
Temperature Coefficient	Absolute	down to ±5ppm to 250ppm	
Resistance Tolerance	Ratio	down to 0,1% (depending on values)	
Resistance Tolerance	Absolute	±0,1% to 30%	
Operating Voltage (max.)	100V		
Power Dissipation (max.)	0,25 Watts per element		
Operating Temperature Range	0 - 70°C		
Insulation Resistance	10.000 MOhm		

Megatron ChipNet and ChipPac offer the absolute freedom to the engineer to design a network using any resistor values. ChipNet can be designed using a wide range of resistor values in combination with any type of chip components. The engineer also has the choice of using chips manufactured with any technology like thick film, thin film or foil. Megatron's ChipNet and ChipPac are truly hybrids with the possibility of combining the passive chips also with active parts like IC's.

Please use for your inquiries and application our form or ask our well trained technical staff!